

Amendments to the Specification

Please **replace** the third full paragraph beginning at page 7, line 28 with the following **rewritten** paragraph:

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In the filtered mode when rejection filter 22 is used, an eight state trellis decoder is required, and in the non-filtered mode when filter 22 is not used, a four state trellis decoder is required, as known. Trellis decoder system 24 (Figure 1) advantageously incorporates a single eight state trellis architecture and seamlessly switches between modes. Decoder 24 provides seamless switching both for the optional filter modes and for data interruptions resulting from program changes and other types of transitions, for example. Trellis decoded data output by decoder 24 is intra-segment symbol deinterleaved by a deinterleaver 755 (not shown). Symbol deinterleaved data from the deinterleaver unit 755 is then further processed by output processor 760 before being passed to other HDTV receiver elements for processing and display, as will be discussed later.